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**Applicants** 

: Mimberg, et al.

Serial No.

: 10/078,292

Examiner: Sun, X.

Filed

: 2/15/02

Group Art Unit: 2863

For

: A METHOD AND SYSTEM FOR DYNAMIC POWER SUPPLY

VOLTAGE ADJUSTMENT FOR A SEMICONDUCTOR

INTEGRATED CIRCUIT DEVICE

## **RESPONSE TO OFFICE ACTION**

Assistant Commissioner for Patents & Trademarks Washington, D.C. 20231

Sir:

In response to the Office Action mailed 01/13/05, please consider the following amendments and remarks:

Attorney Docket No. NVID-P000406 Serial No. 10/078,292

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